

PG161-A00

TU106 6GB GDDR6, 192b, X16
DVI-D/DP + DP + HDMI

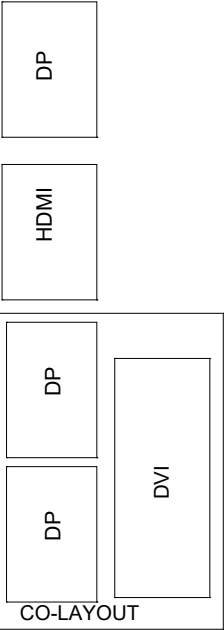
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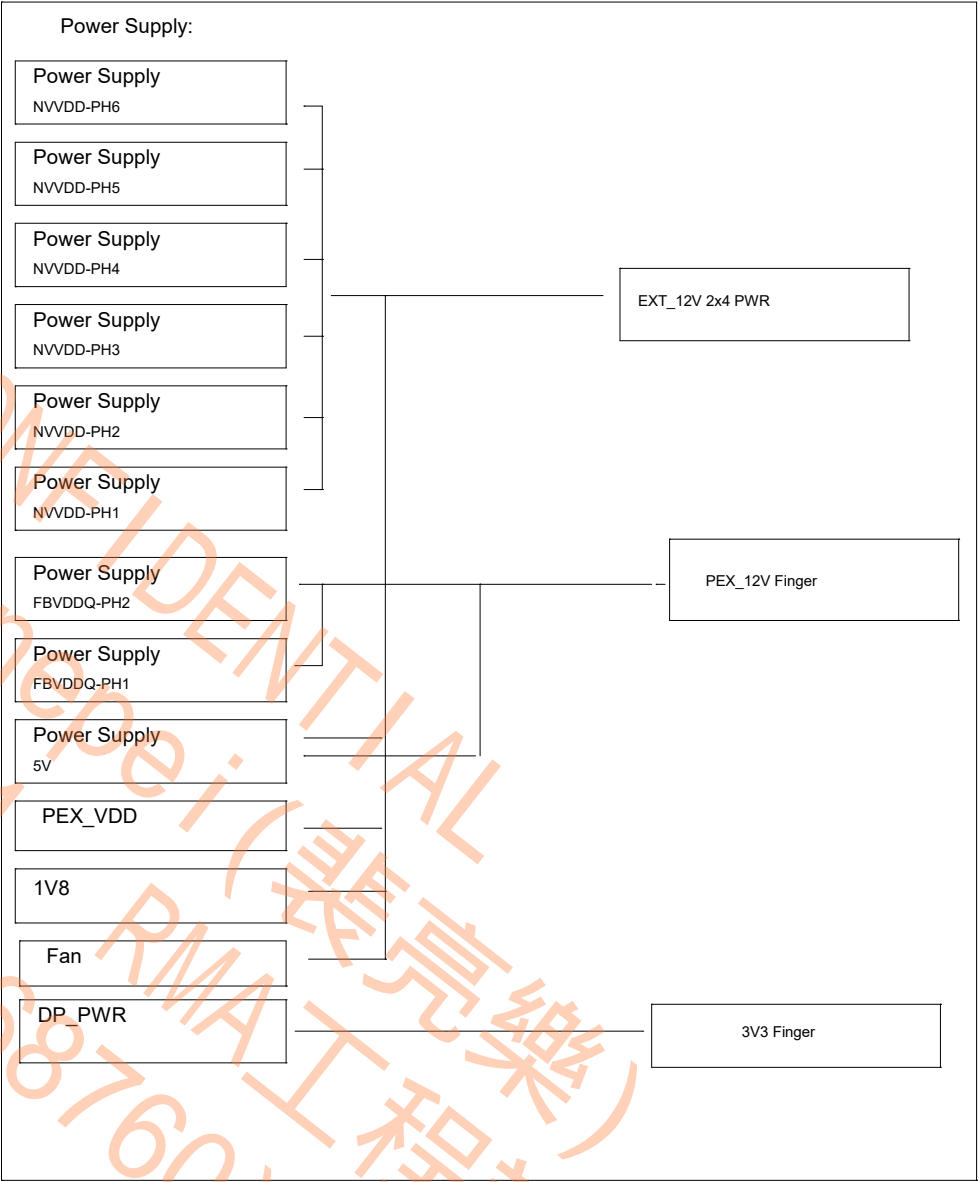
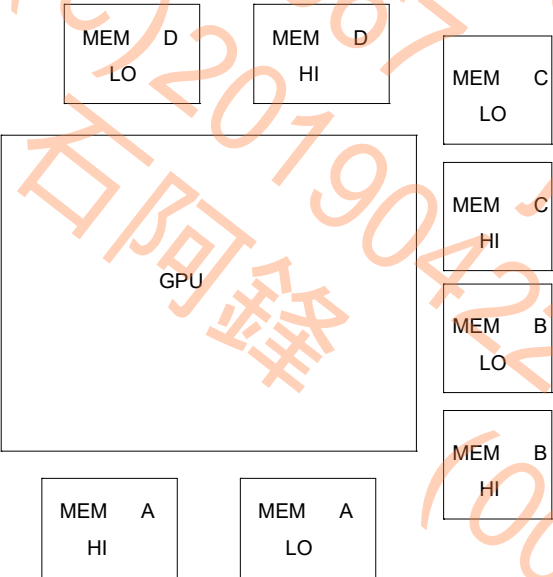
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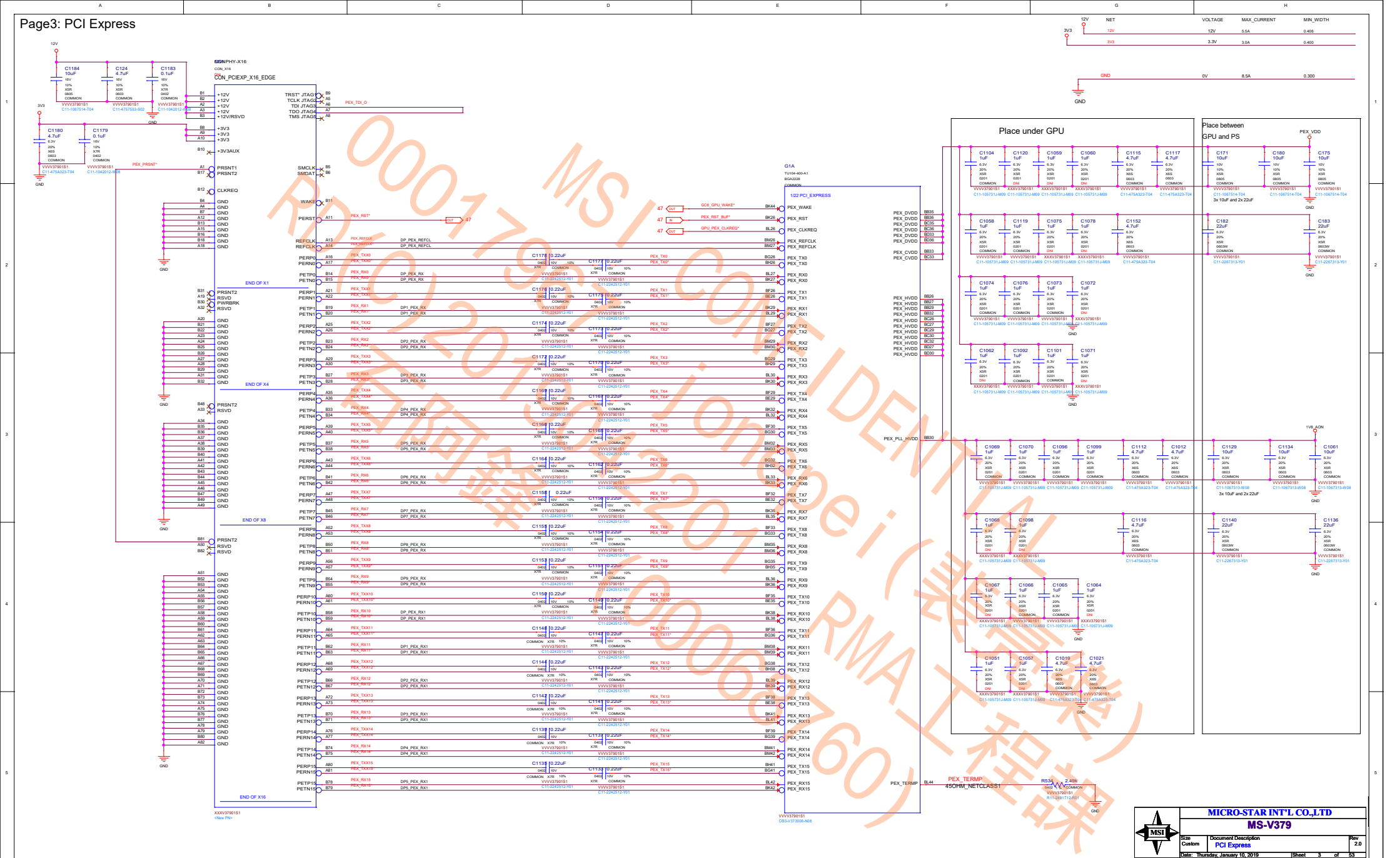
V379-2.0 Change List (base on V375-2.1) :

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6	change to GDDR5
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25	Add GPIO10_FBVREF_SEL



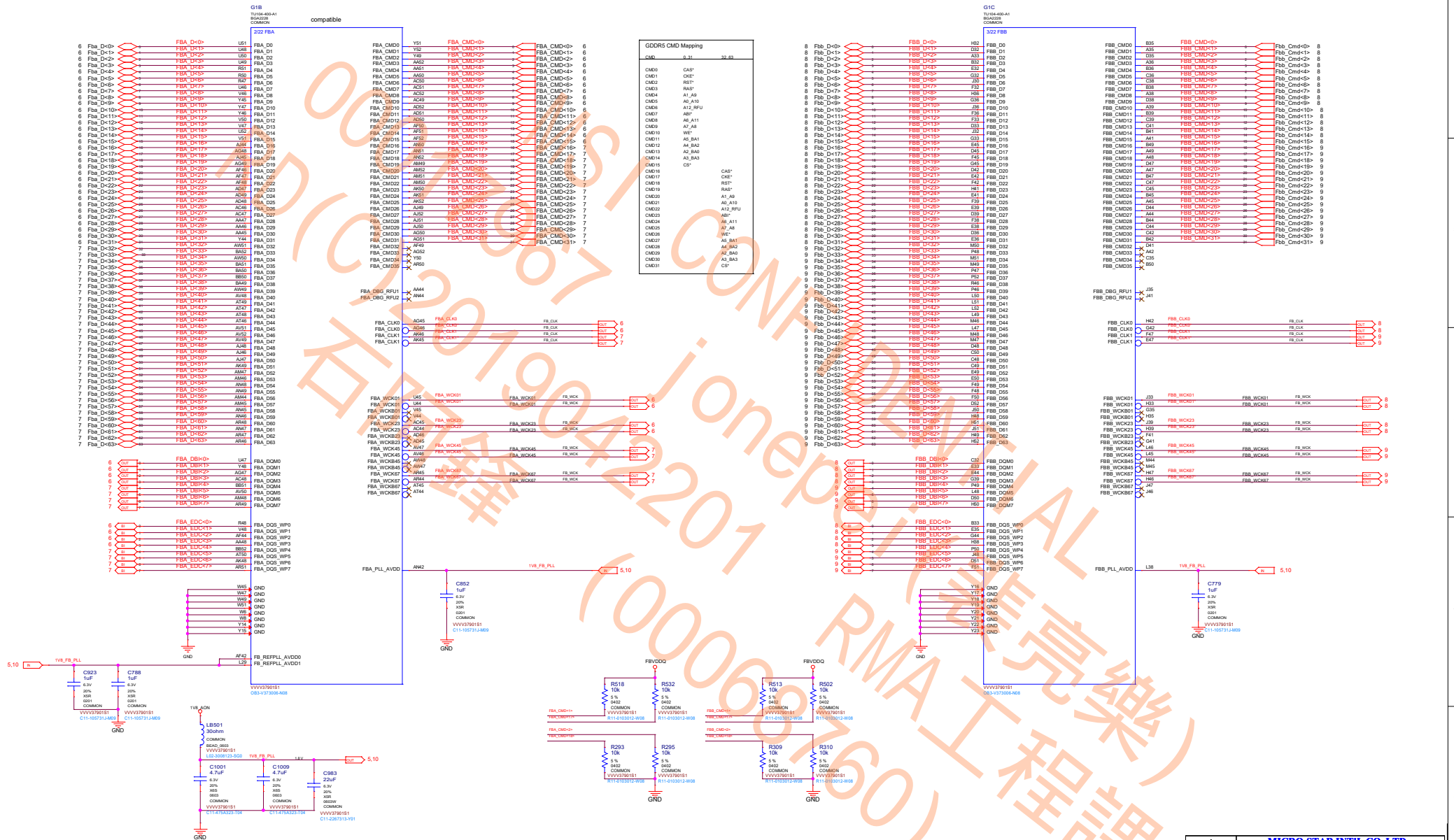
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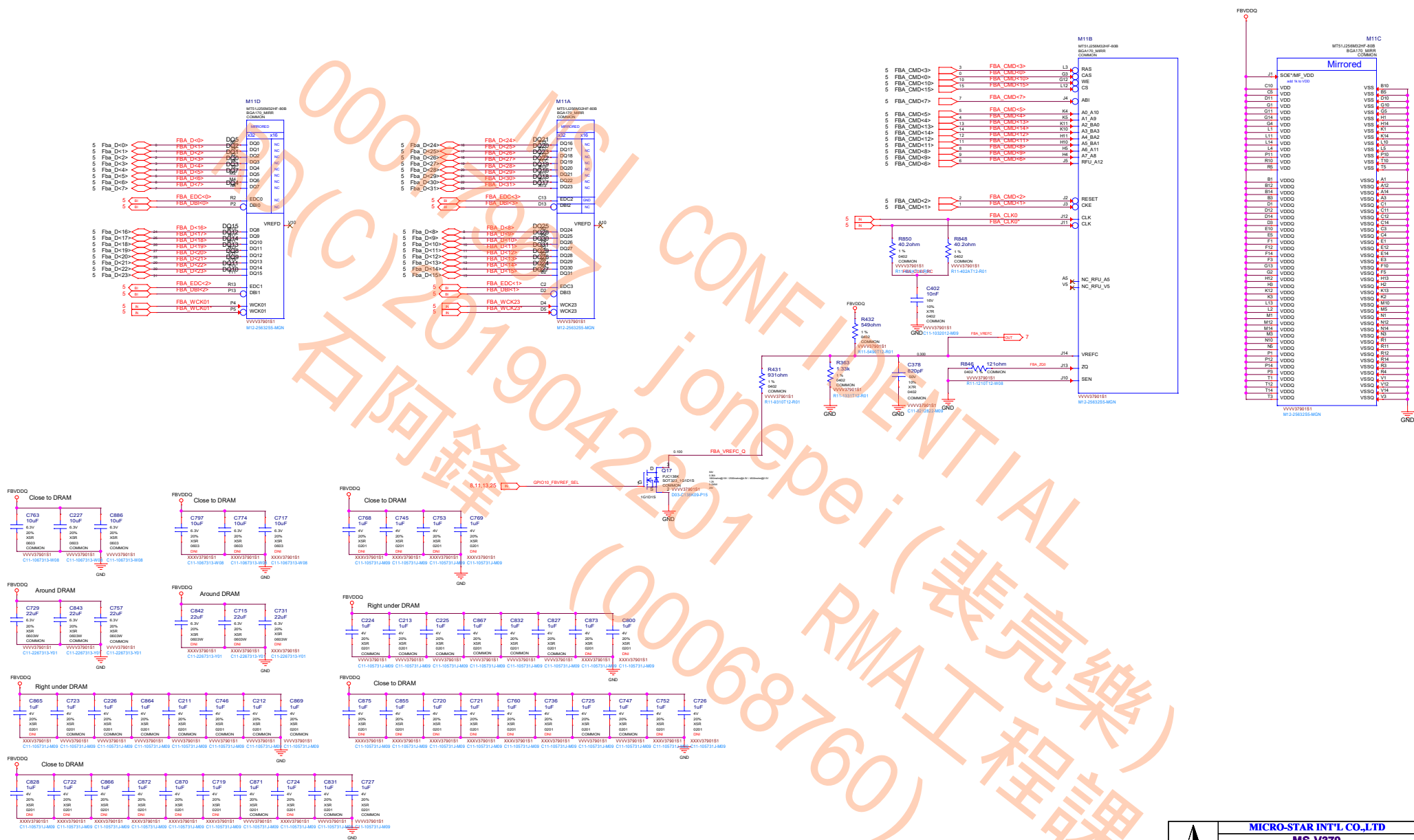


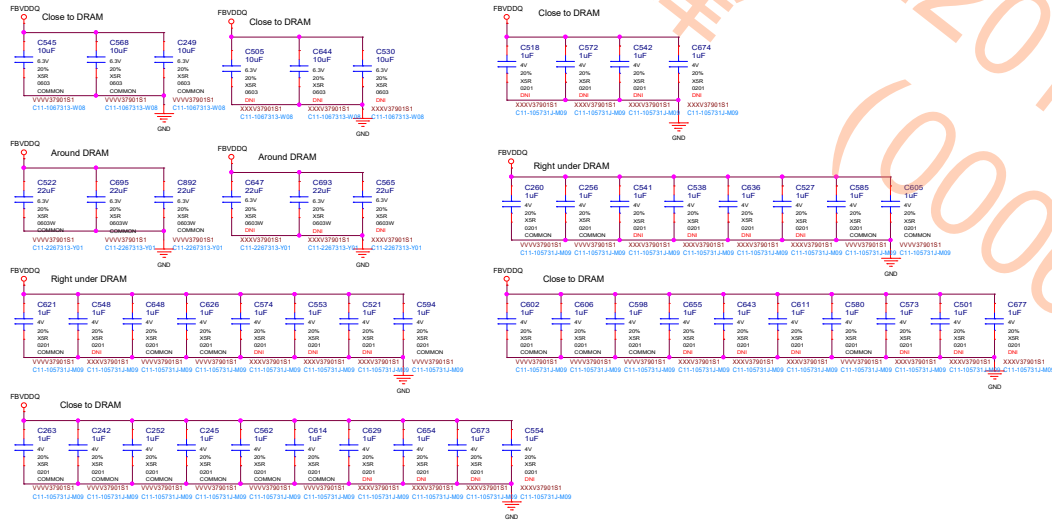
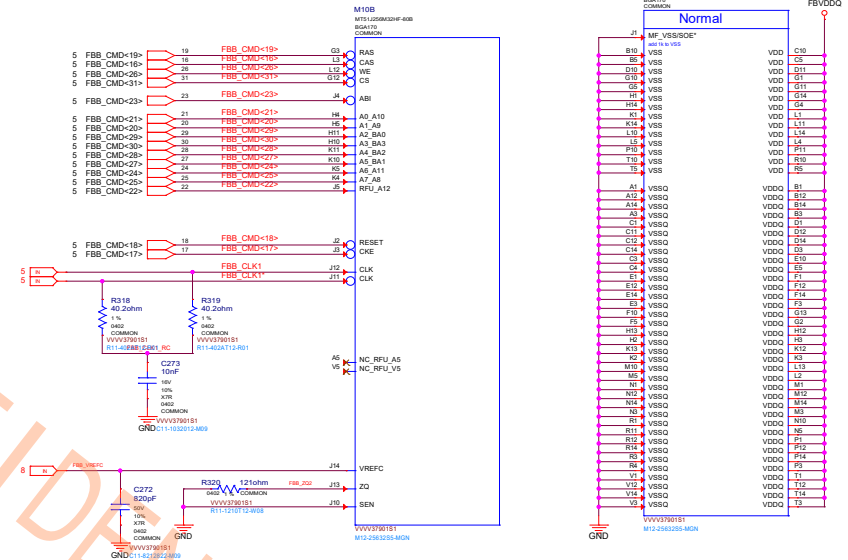
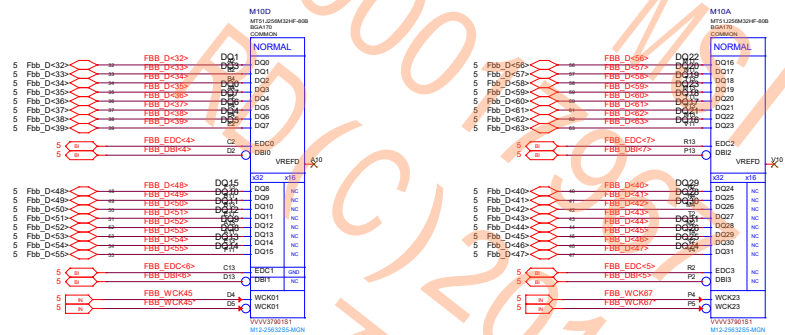


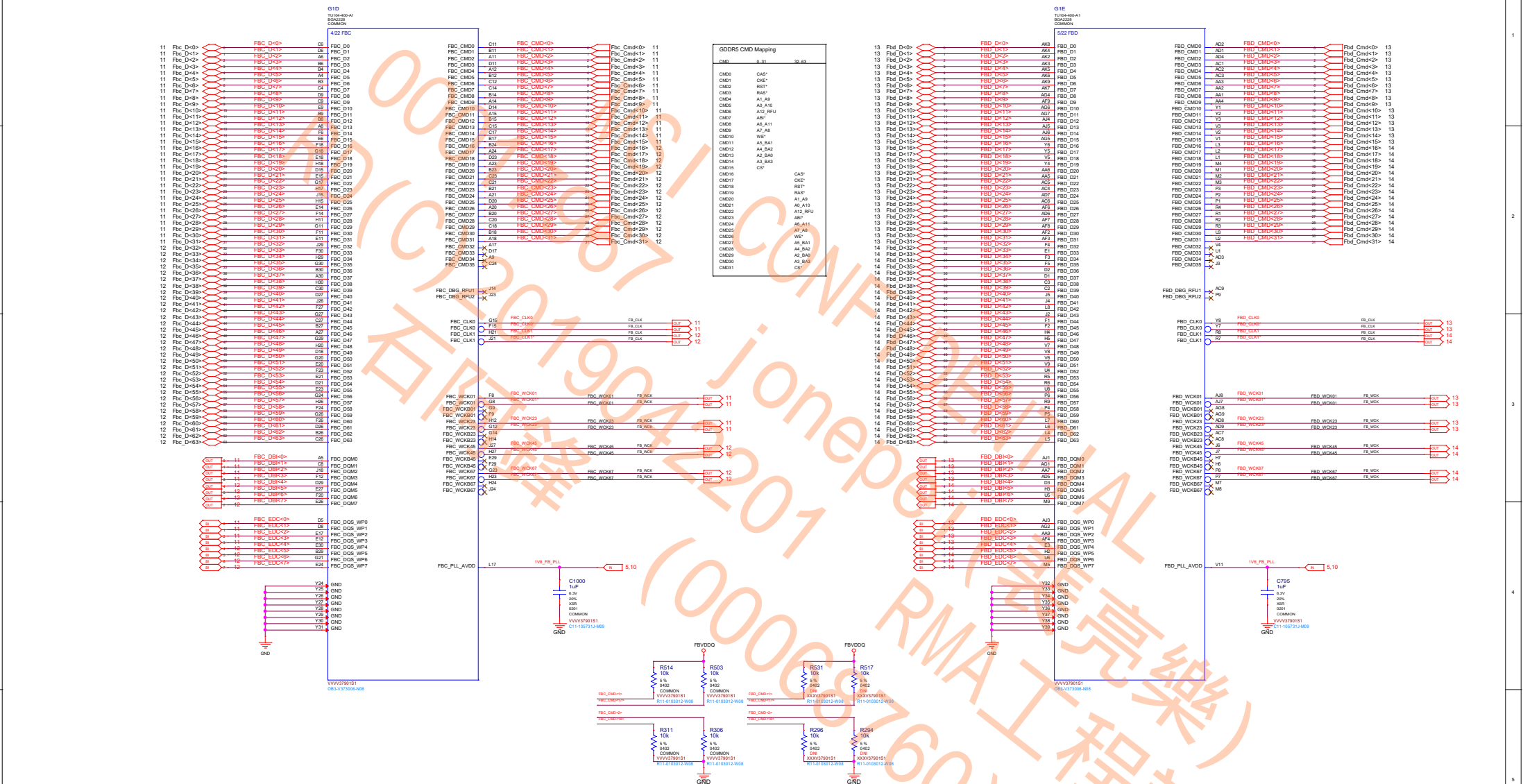
NO PEX GEN4 SUPPORT
DELETE RC TERMINATIONS

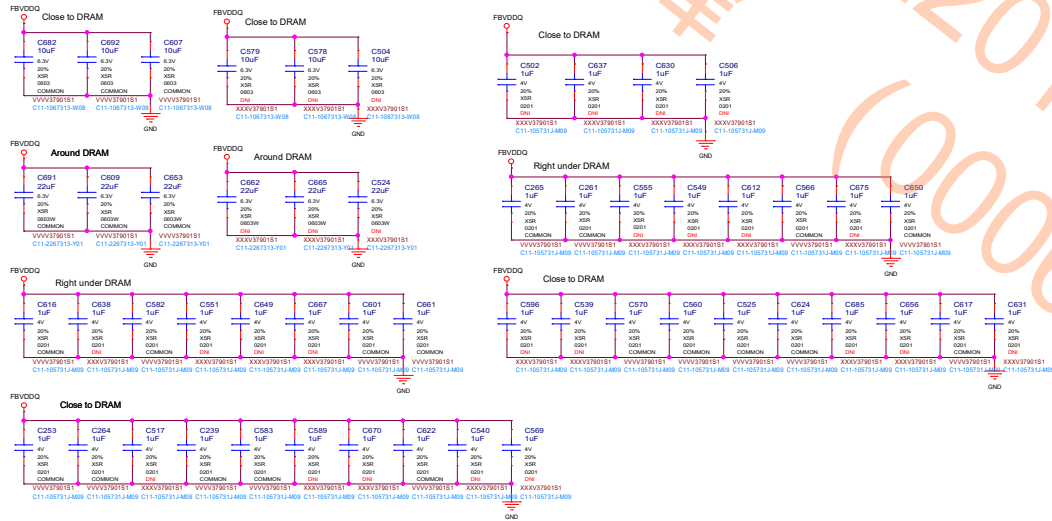
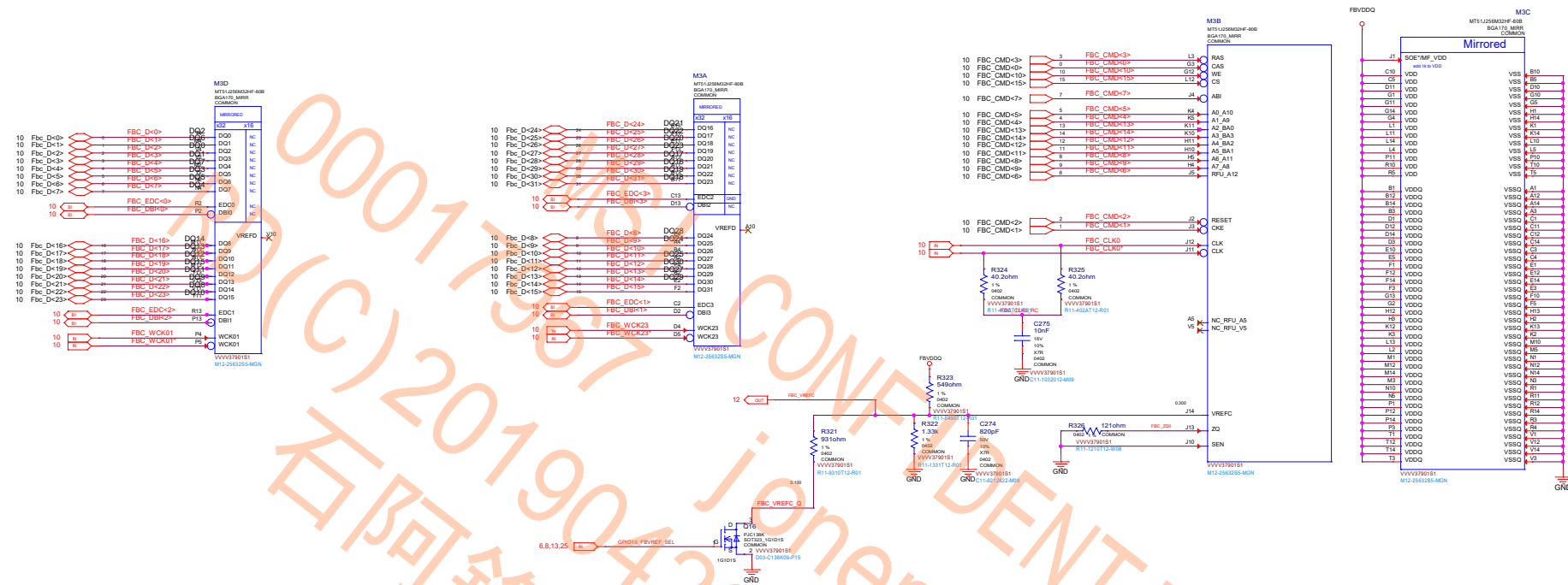
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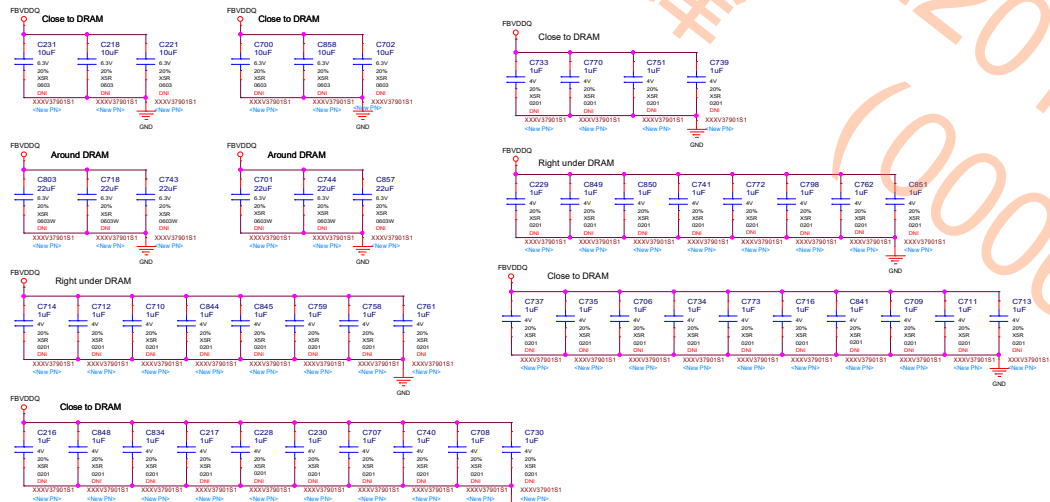


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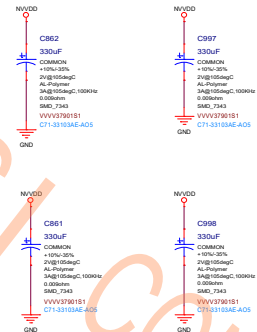
Size Custom	Document Description MEMORY_FBD[63_32]	Rev 2.0
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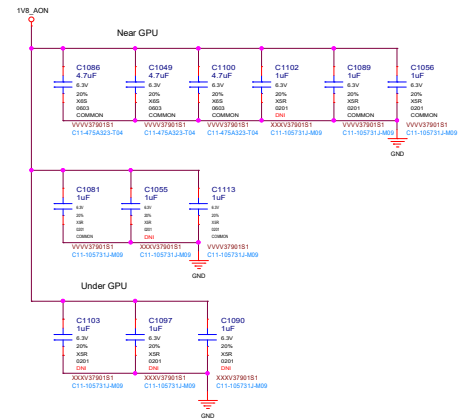
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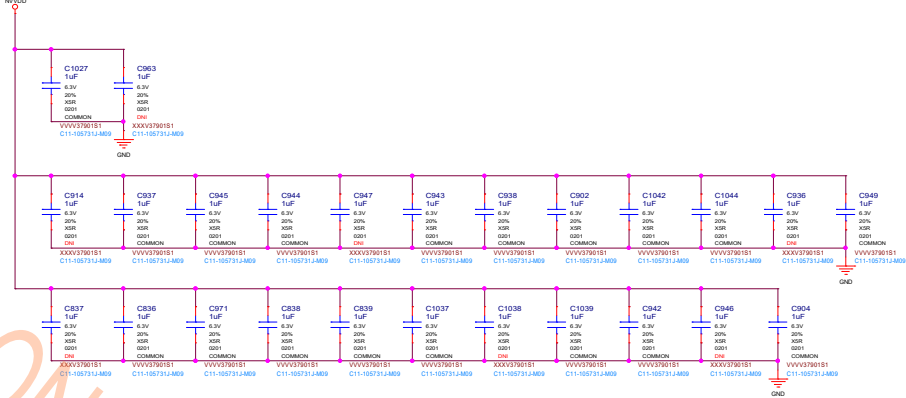
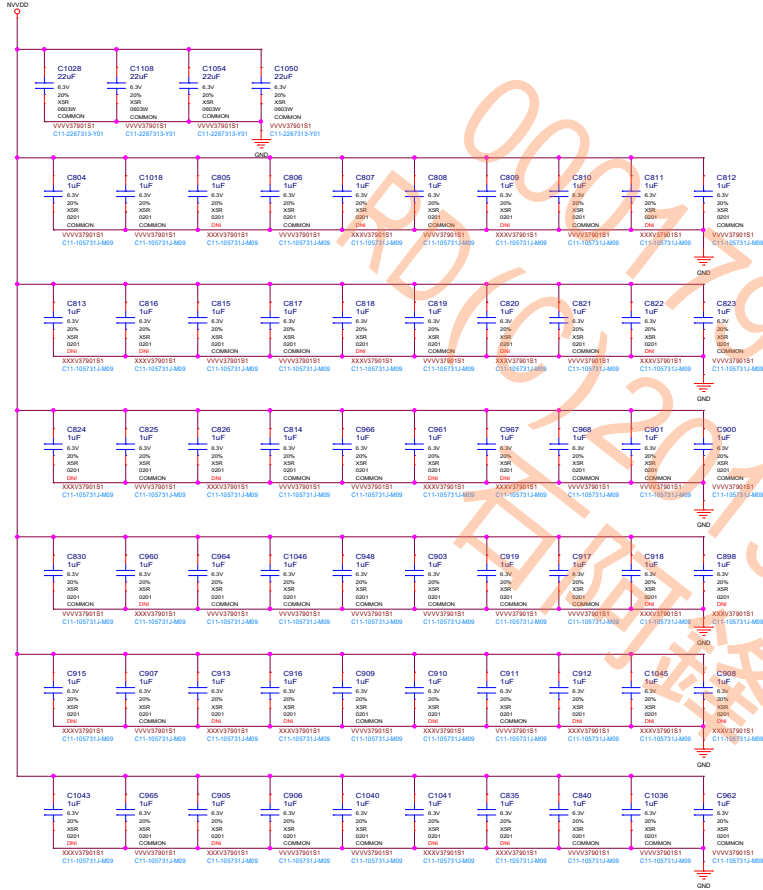
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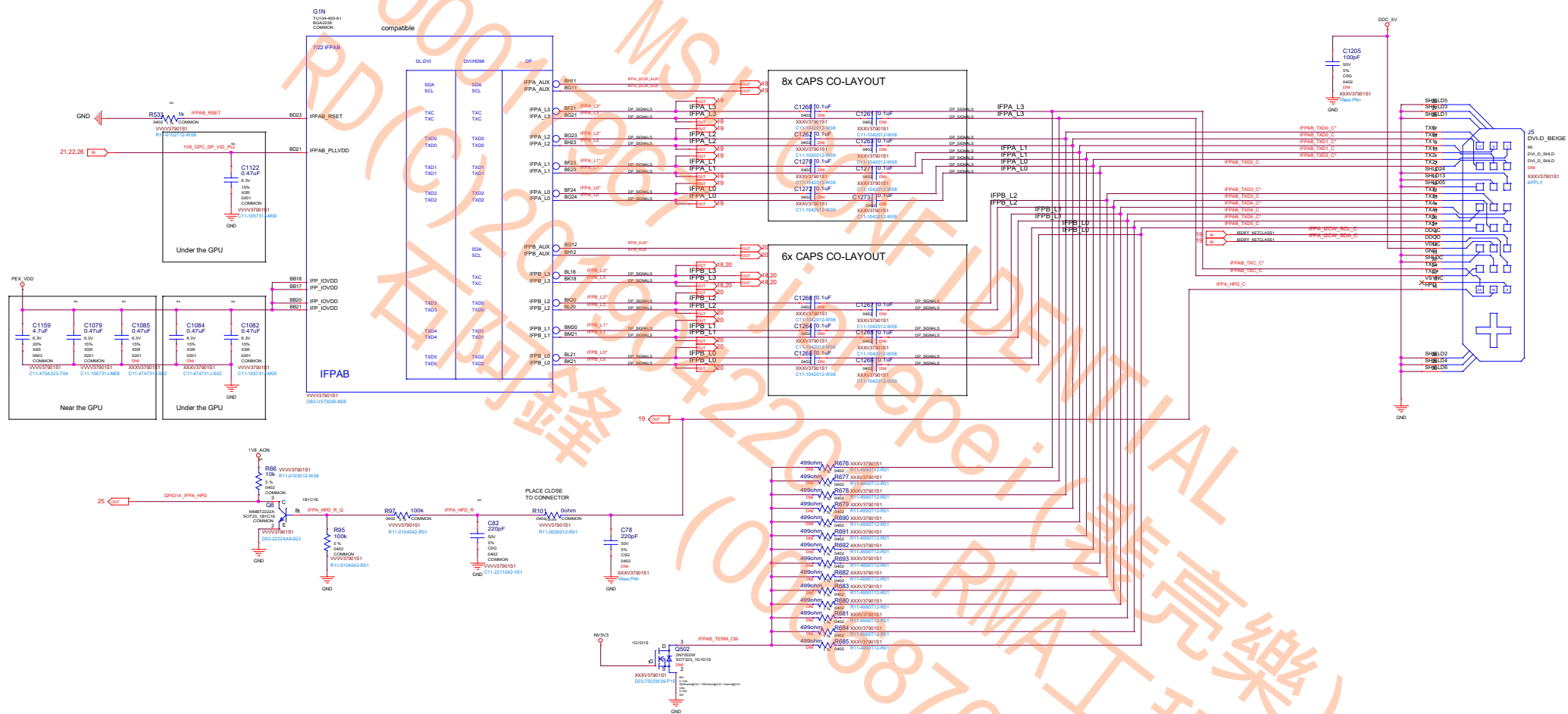
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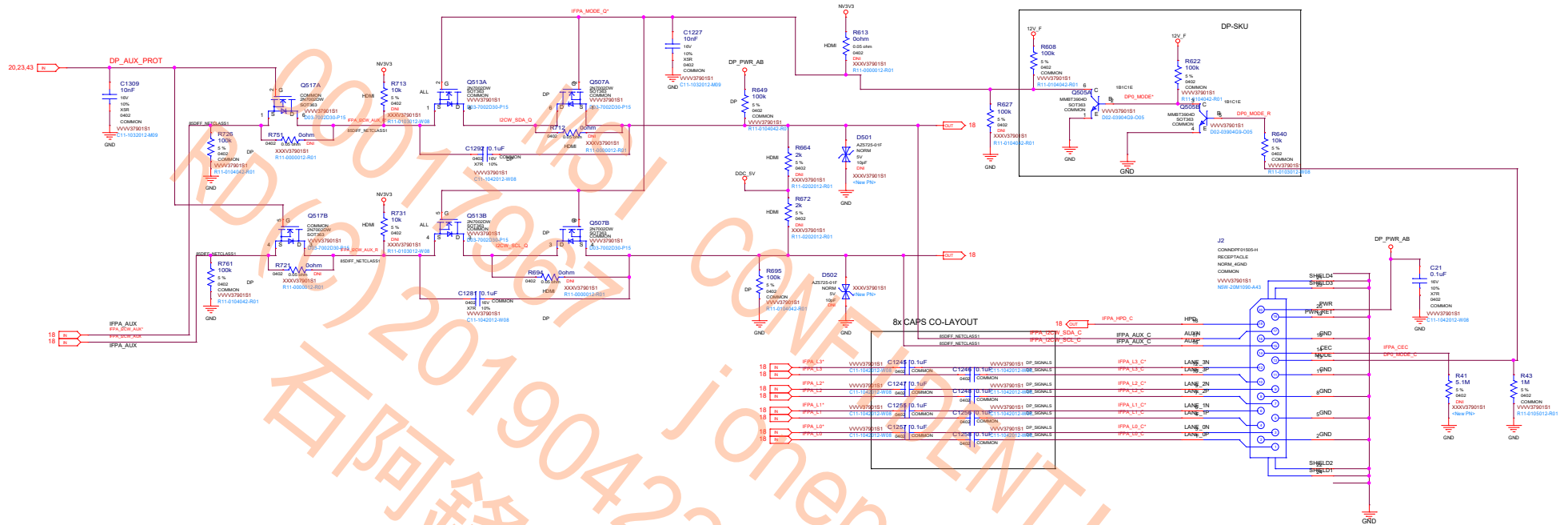


NVDD



IFPAB DVI-D-DL CO-LAYOUT WITH 2x DP



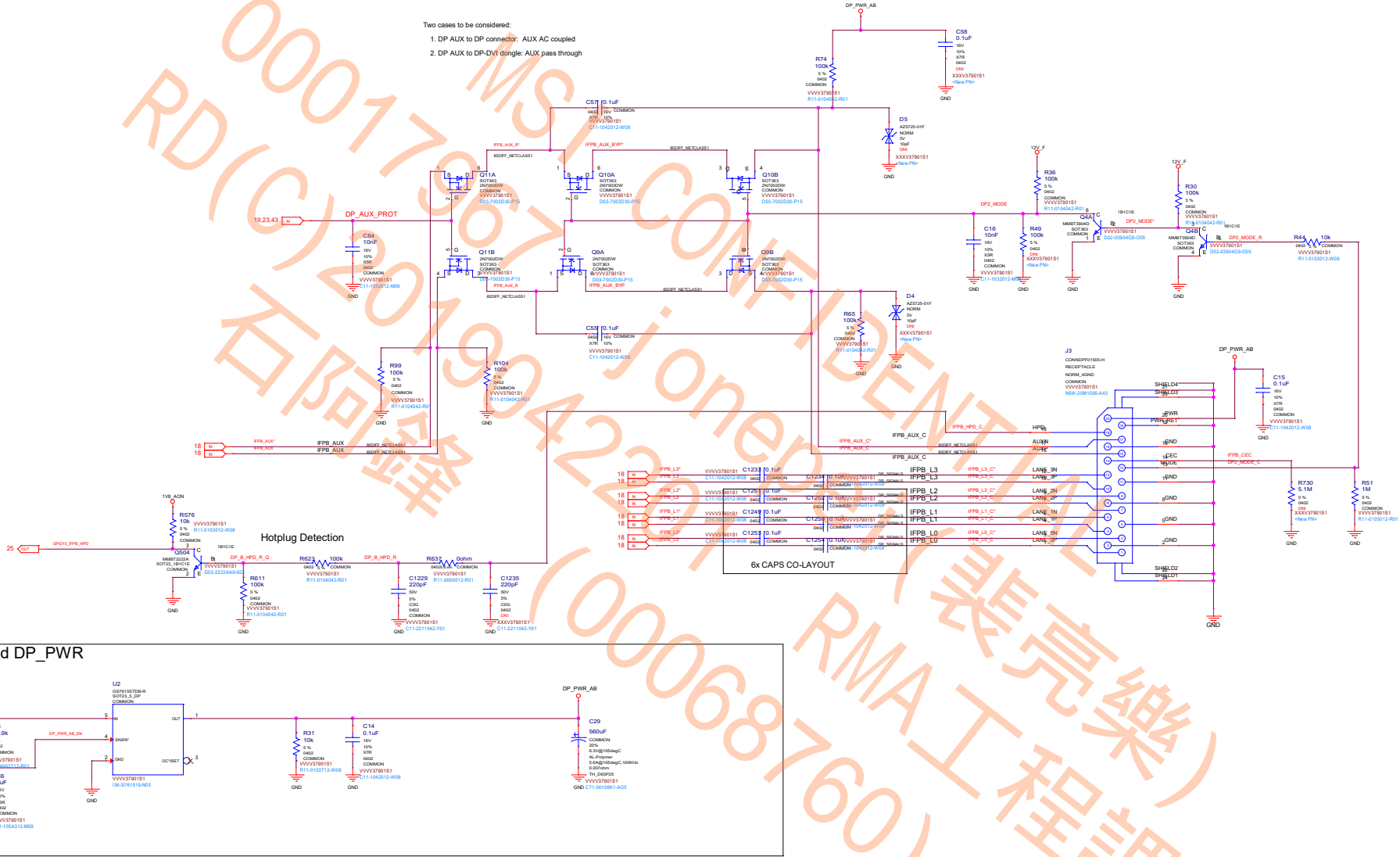


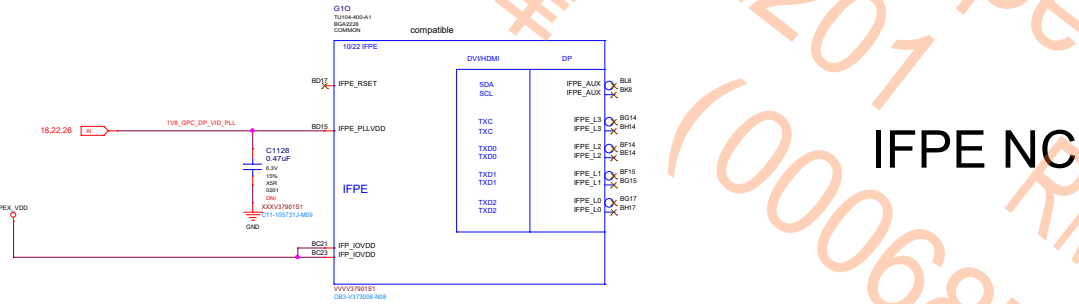
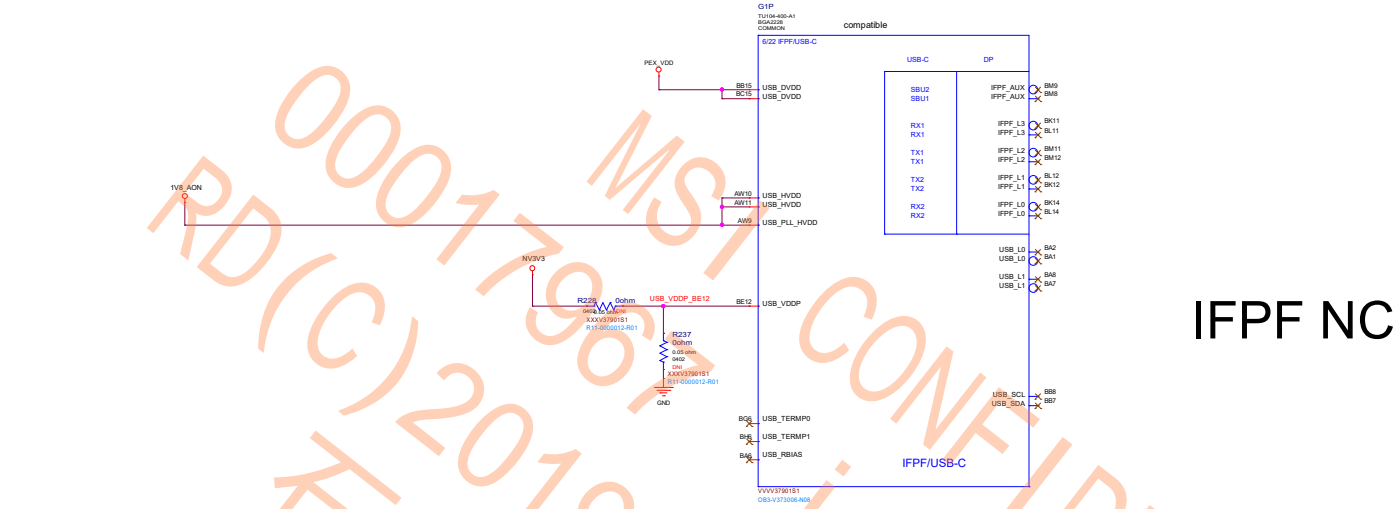
IFPA DP CO-LAYOUT WITH IFPAB DVI-D-DL

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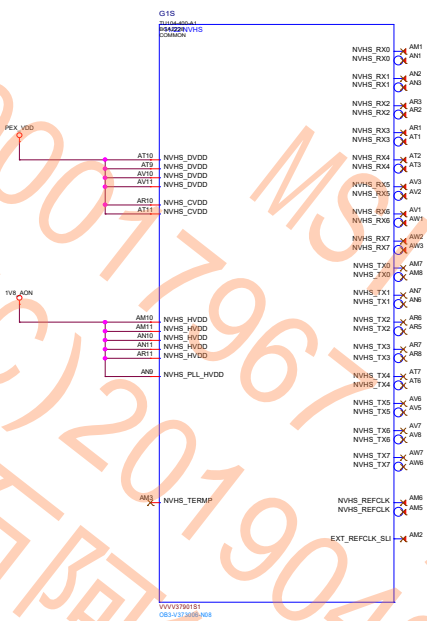
IFPB DP CO-LAYOUT WITH IFPAB DVI-D-DL

- Two cases to be considered:
- 1. DP AUX to DP connector: AUX AC coupled
 - 2. DP AUX to DP-DVI dongle: AUX pass through



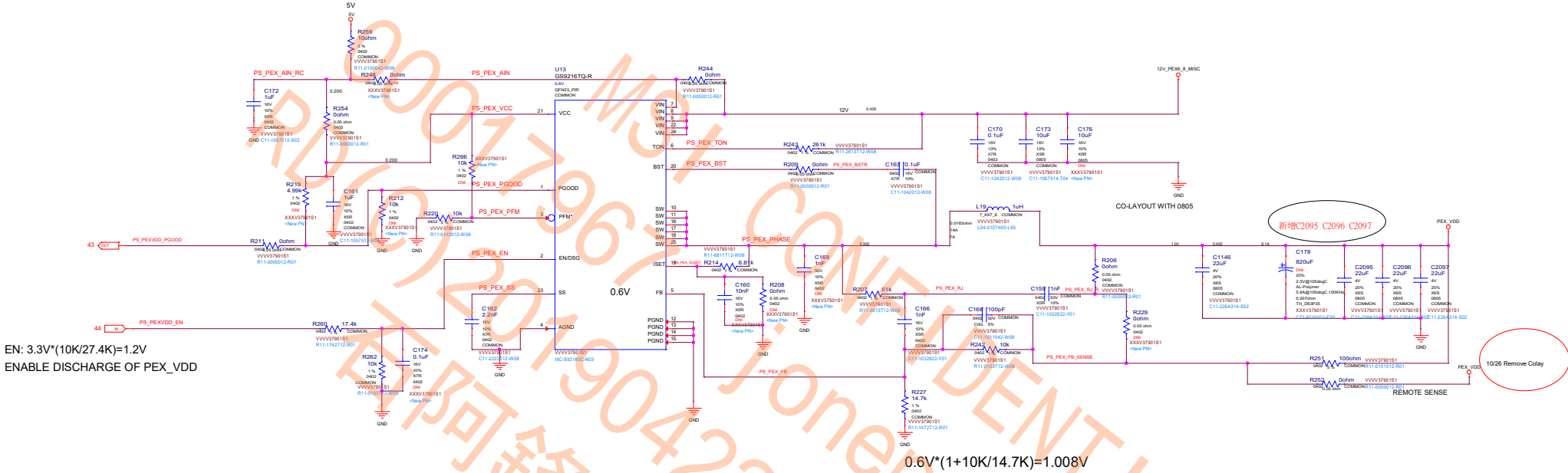


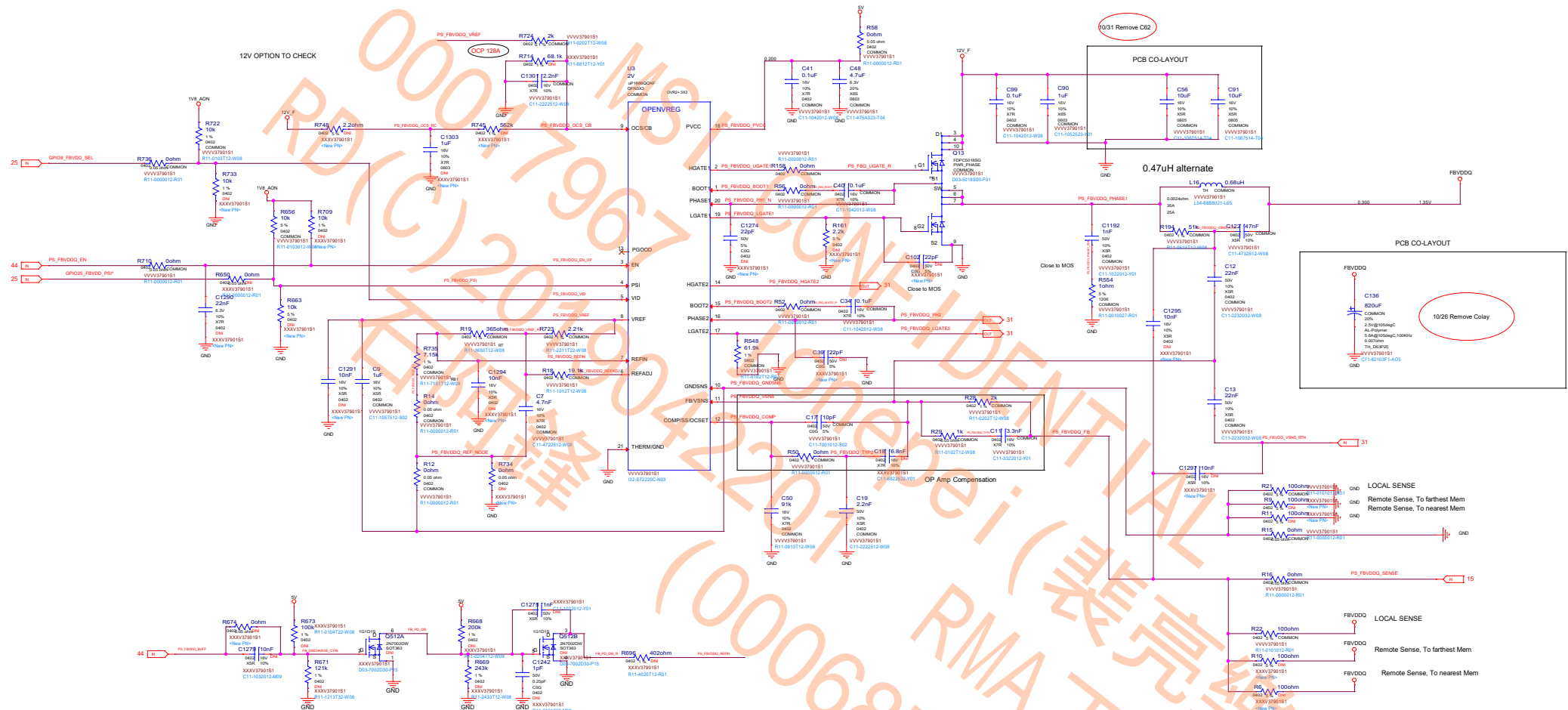
00077967 MS RD(C)2019042201 石阿鋒 jonepei (00068760) CONFIDENTIAL (裴亮樂) RMA工程課

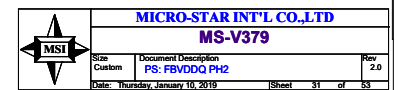
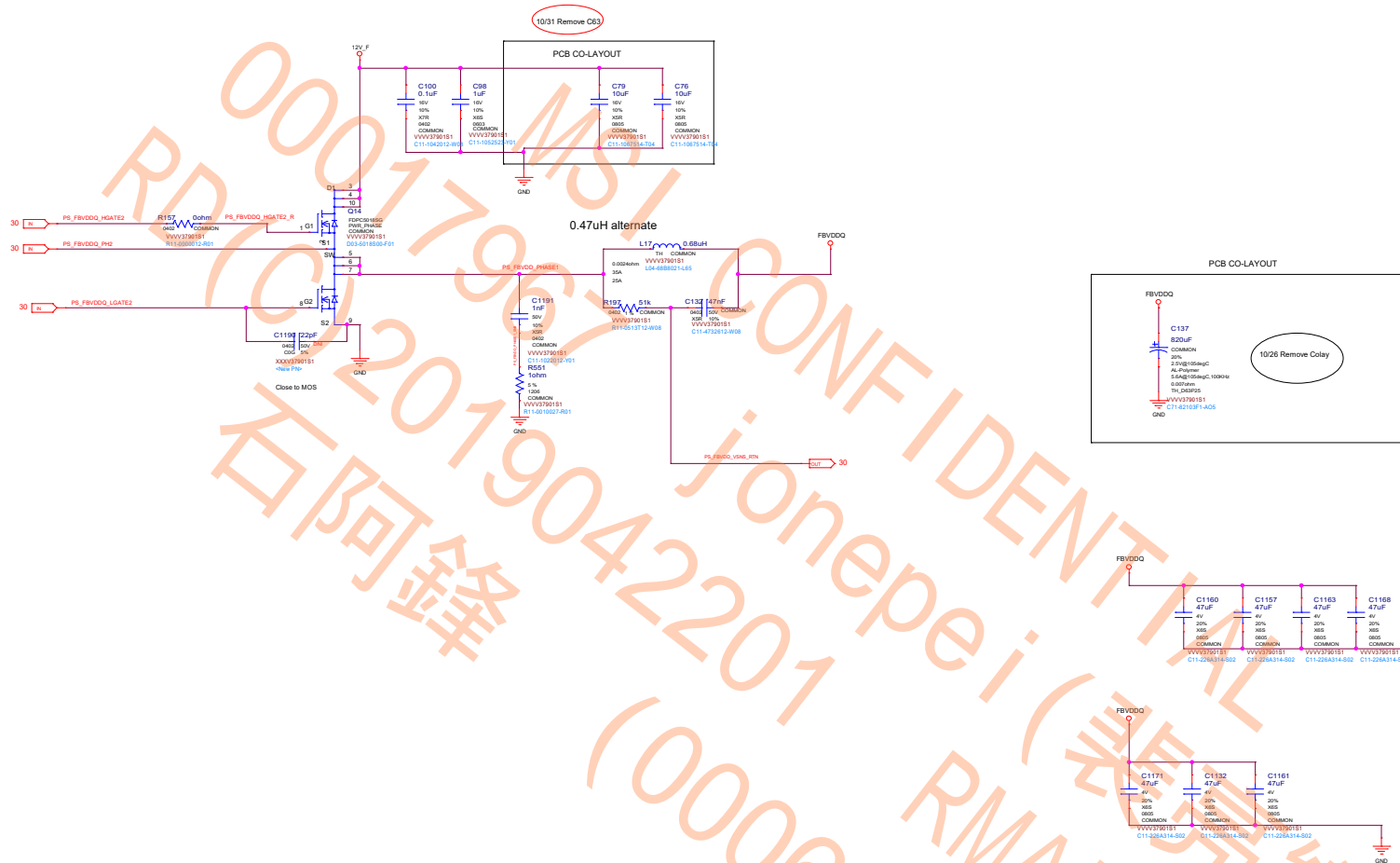


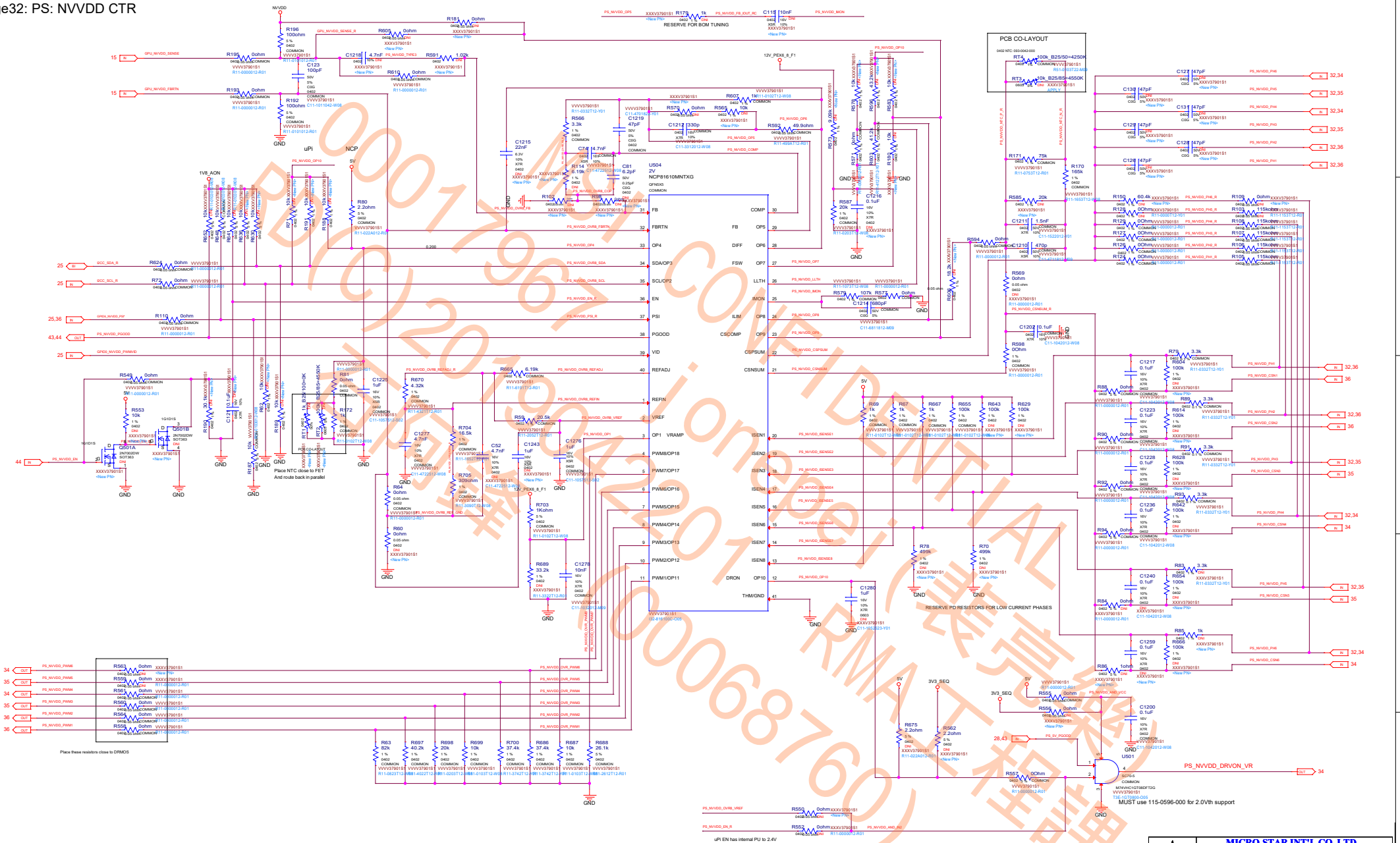


PEX Switcher









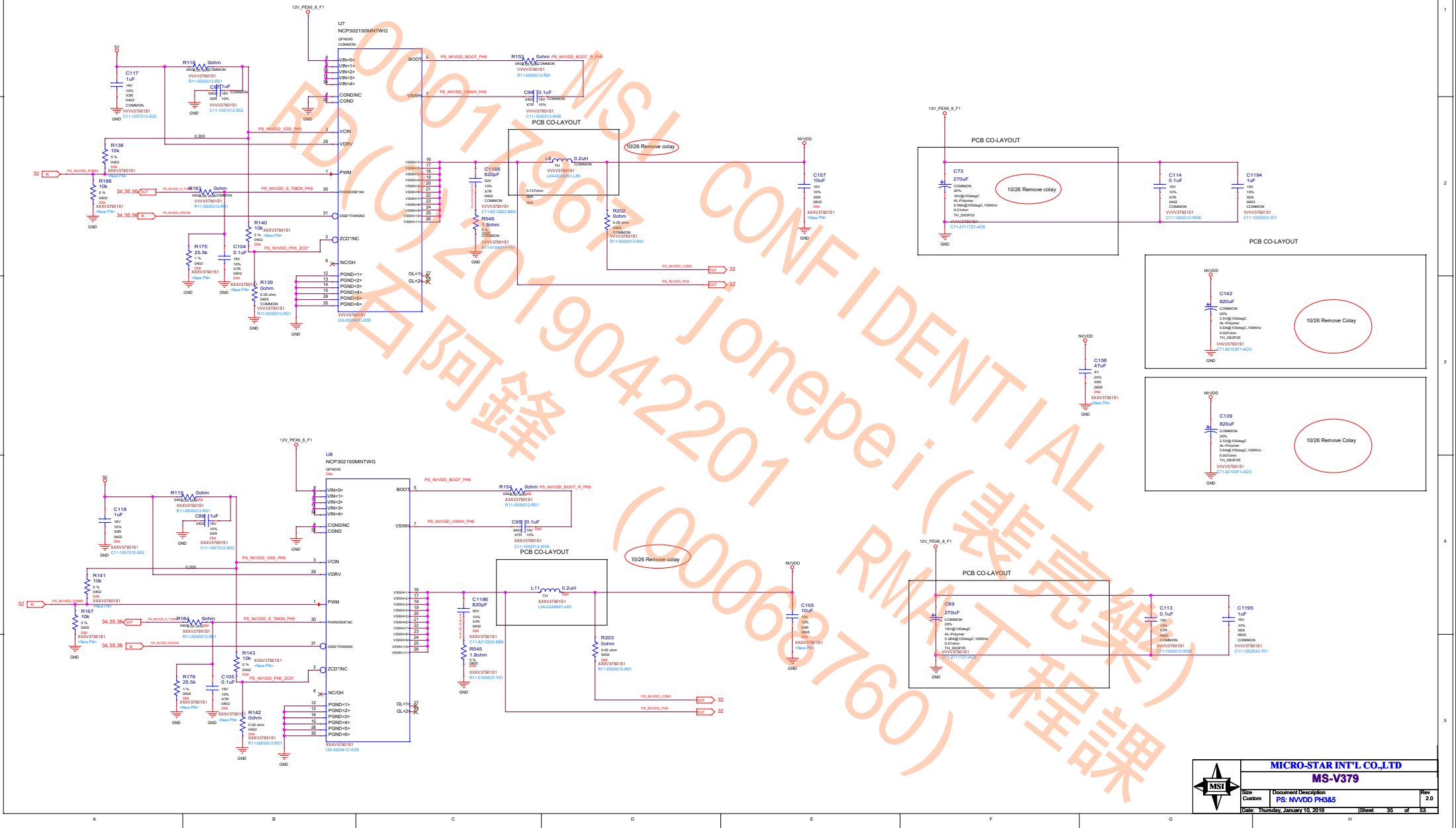
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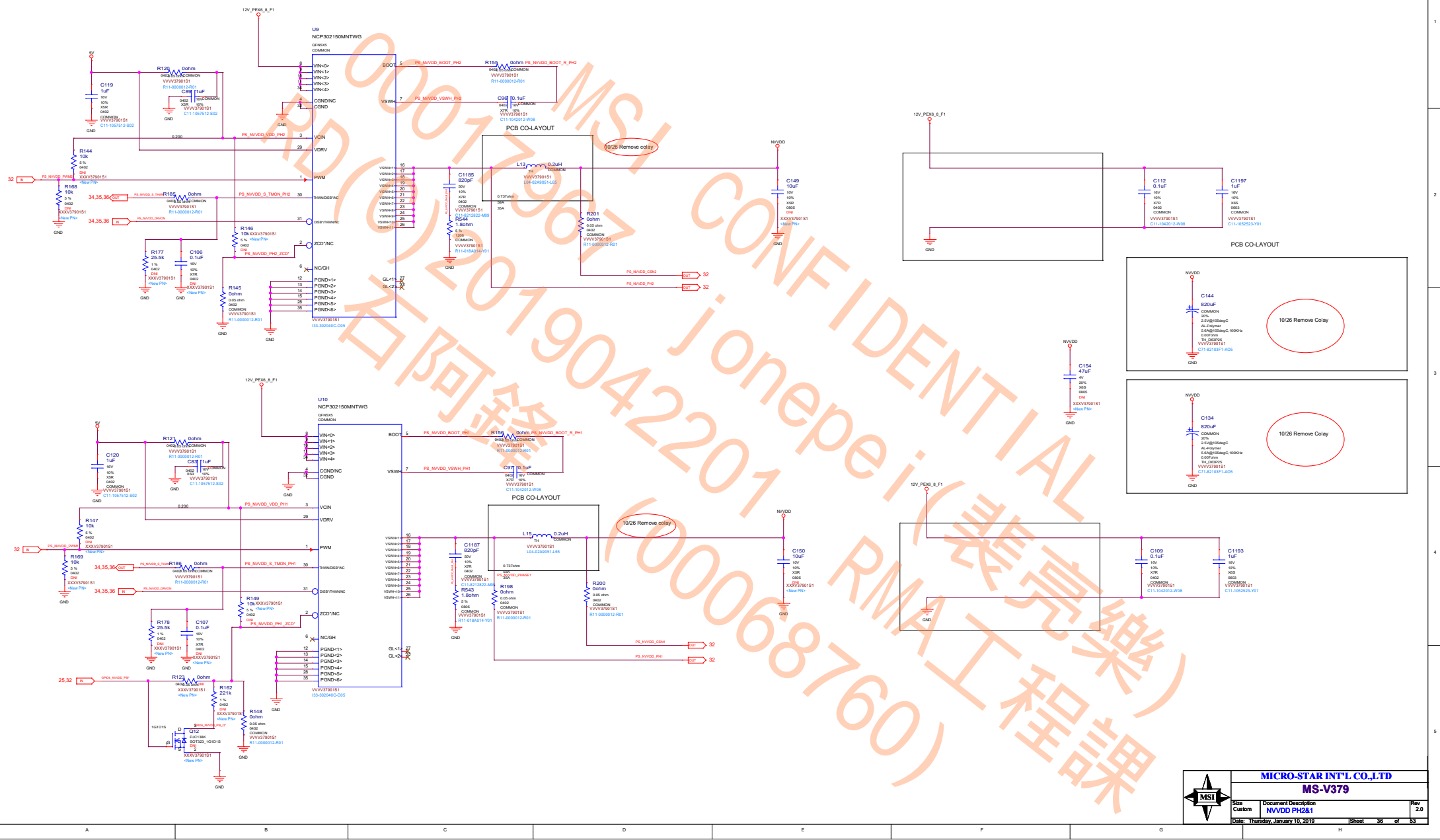
Size Custom	Document Description PS: NWDD CTR	Rev 2.0
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DROP OVR3i SOLUTION

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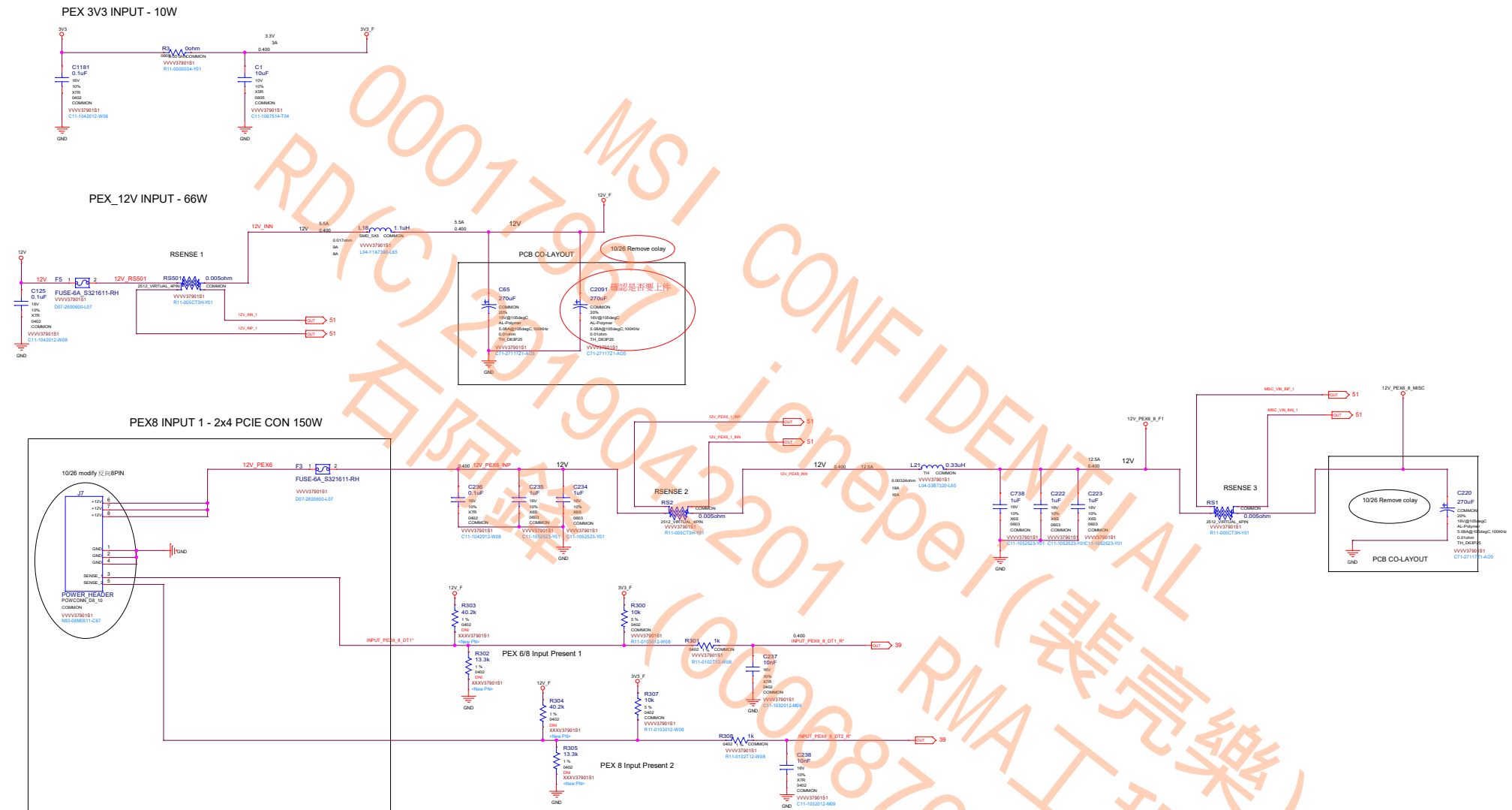
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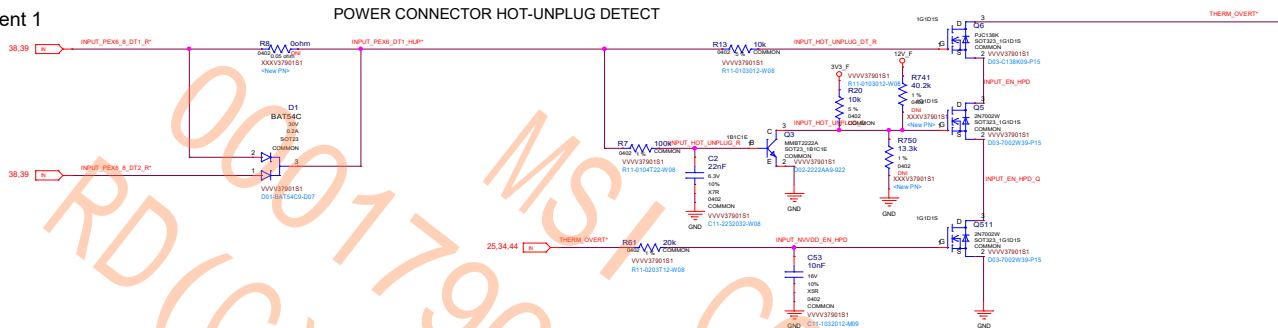
DELETE RAIL BALANCE CIRCUIT

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Custom	Input Power Balancing Switcher	2.0
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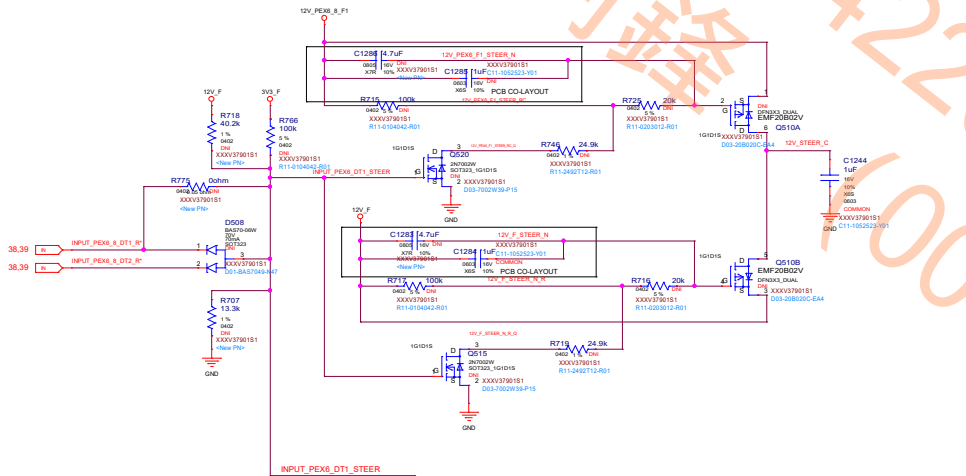
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PEX Input Present 1

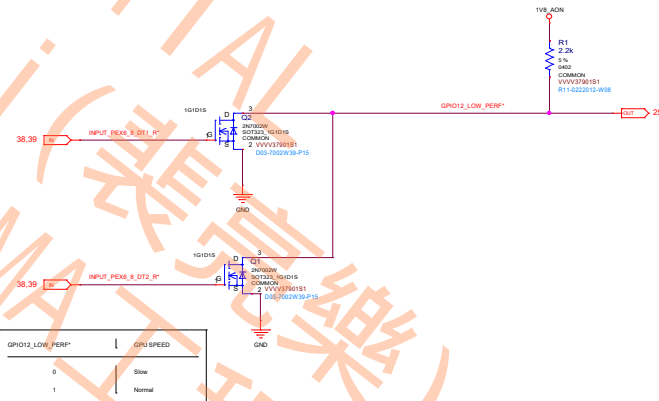


12V CURRENT STEERING (UNDER POWER BOOT):


GUIDES CURRENT FROM PEX EDGE TO PEX 6 PIN INPUT AREA




PEX Input - Power Level/PSI* Control



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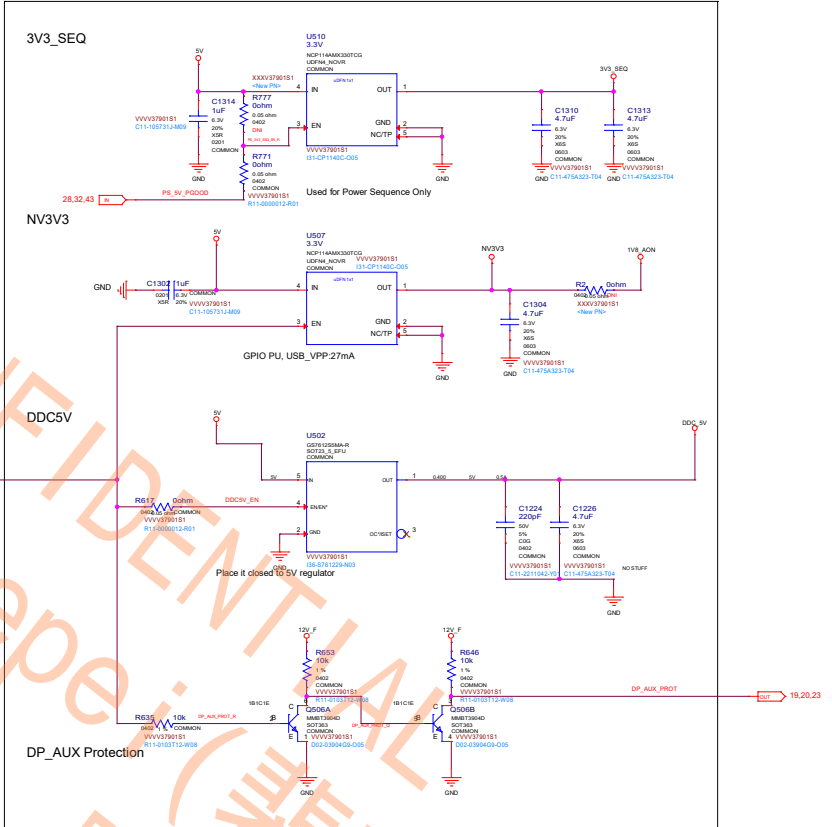
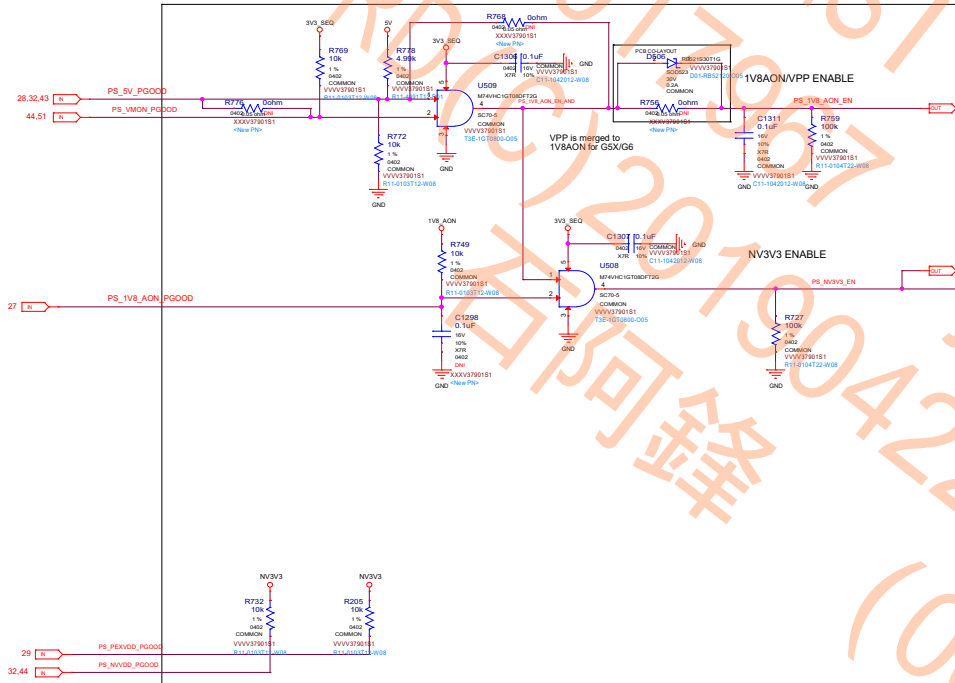
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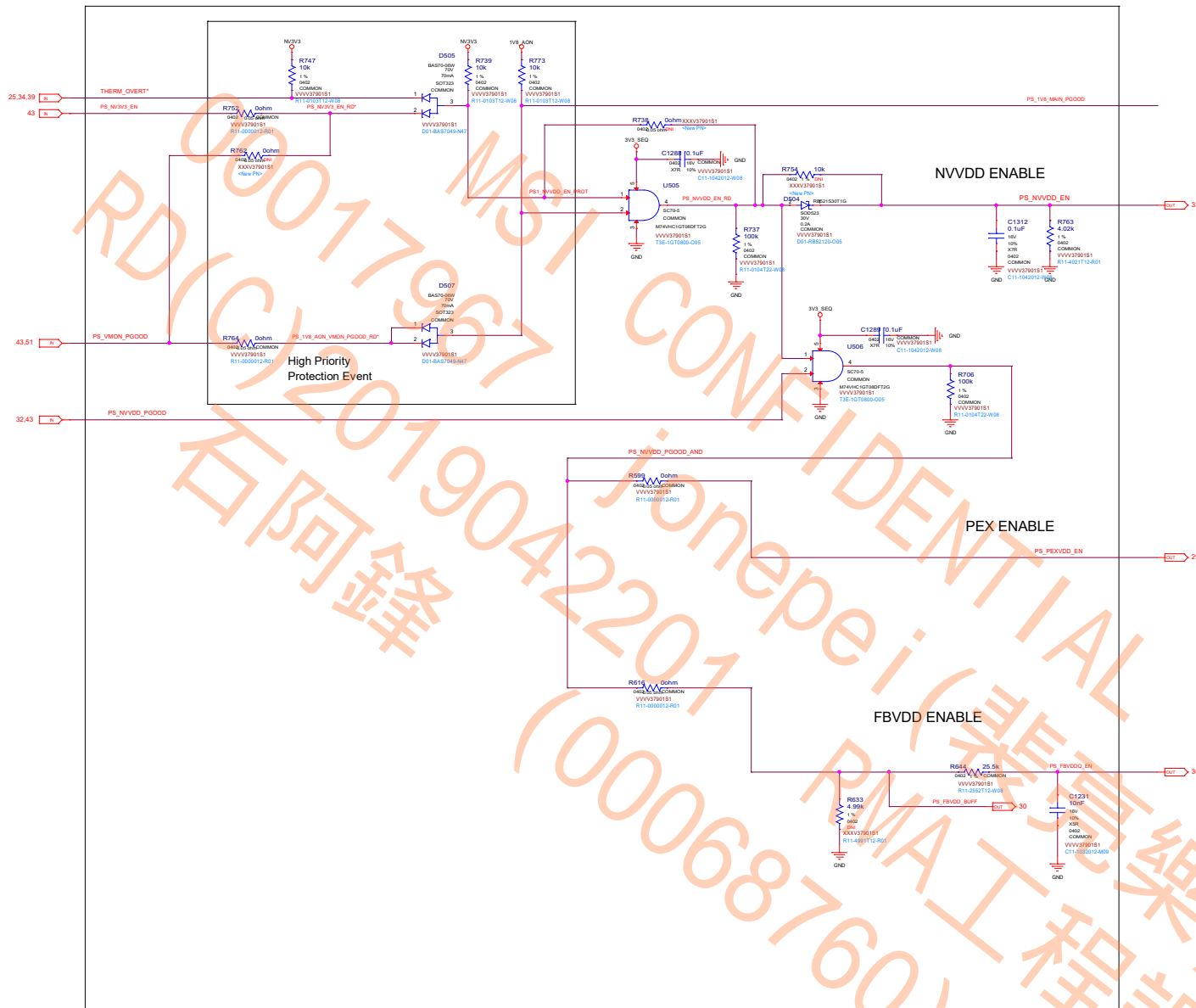
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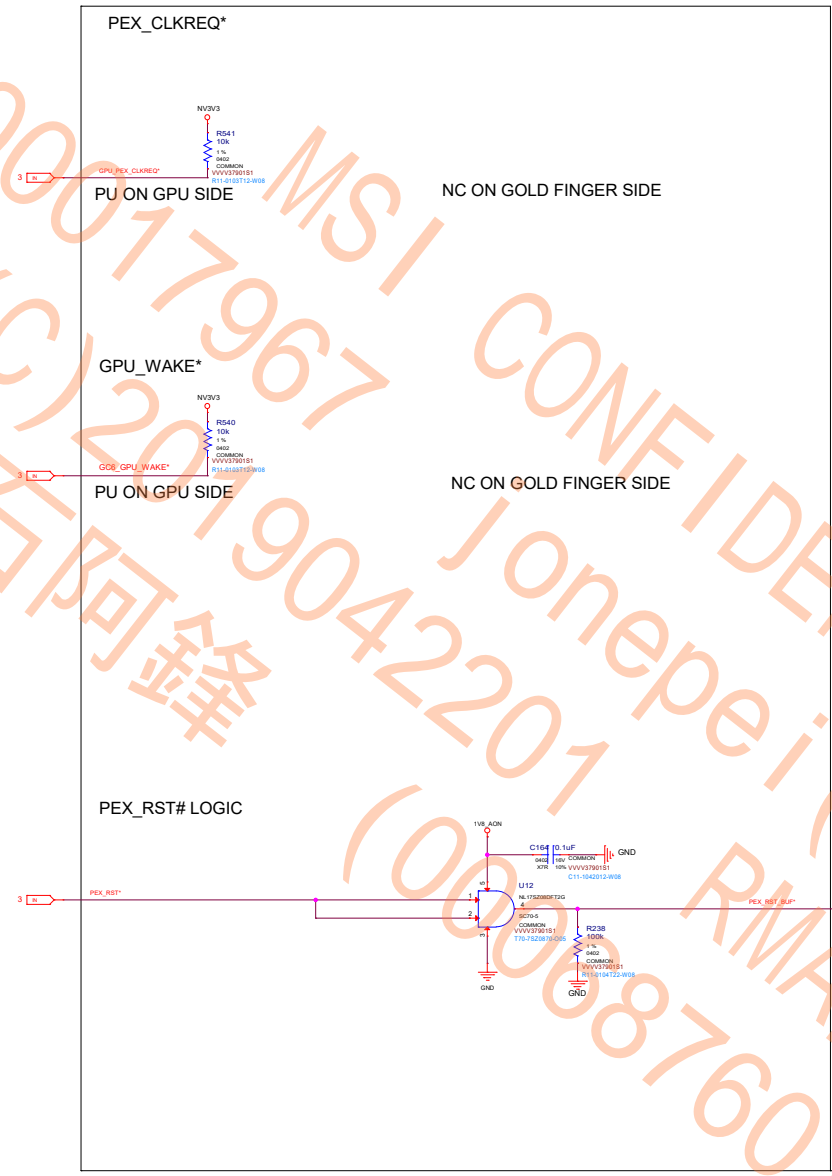


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	MICRO-STAR INT'L CO.,LTD	
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SEQUENCE:Voltage Monitor		
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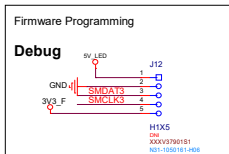
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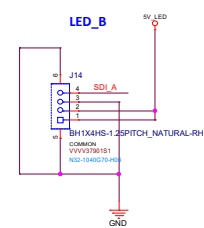
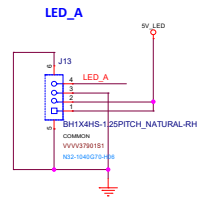
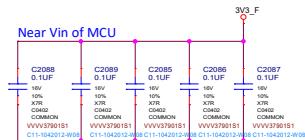
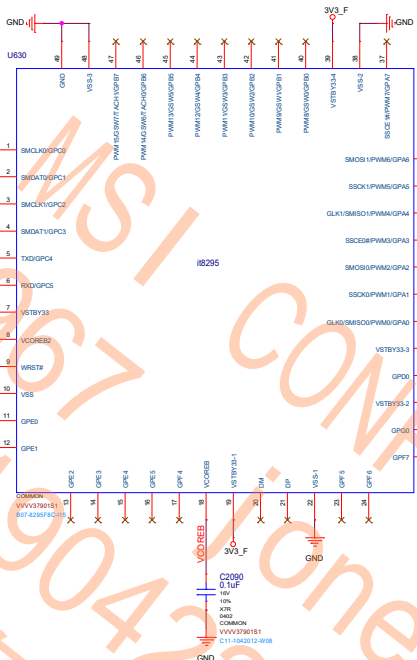
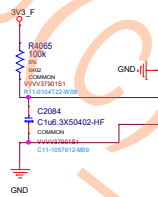
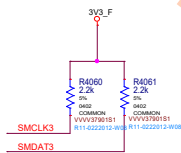


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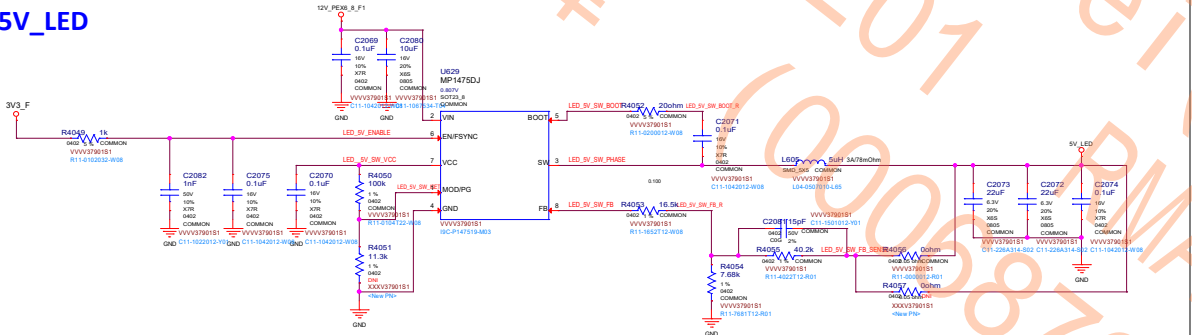
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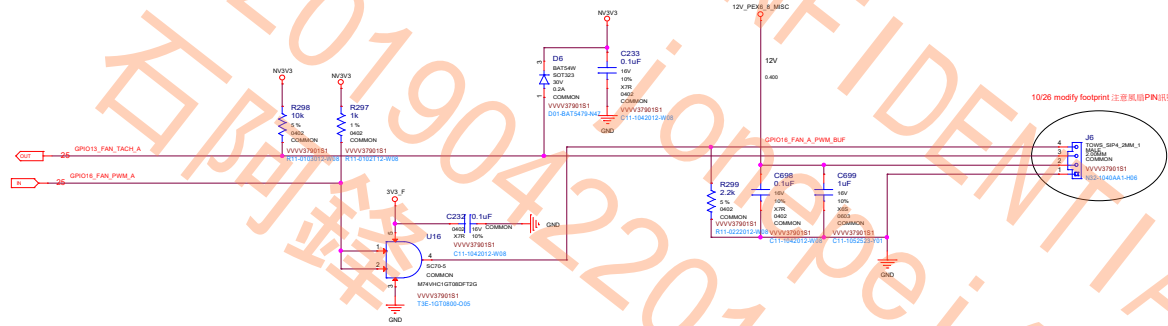


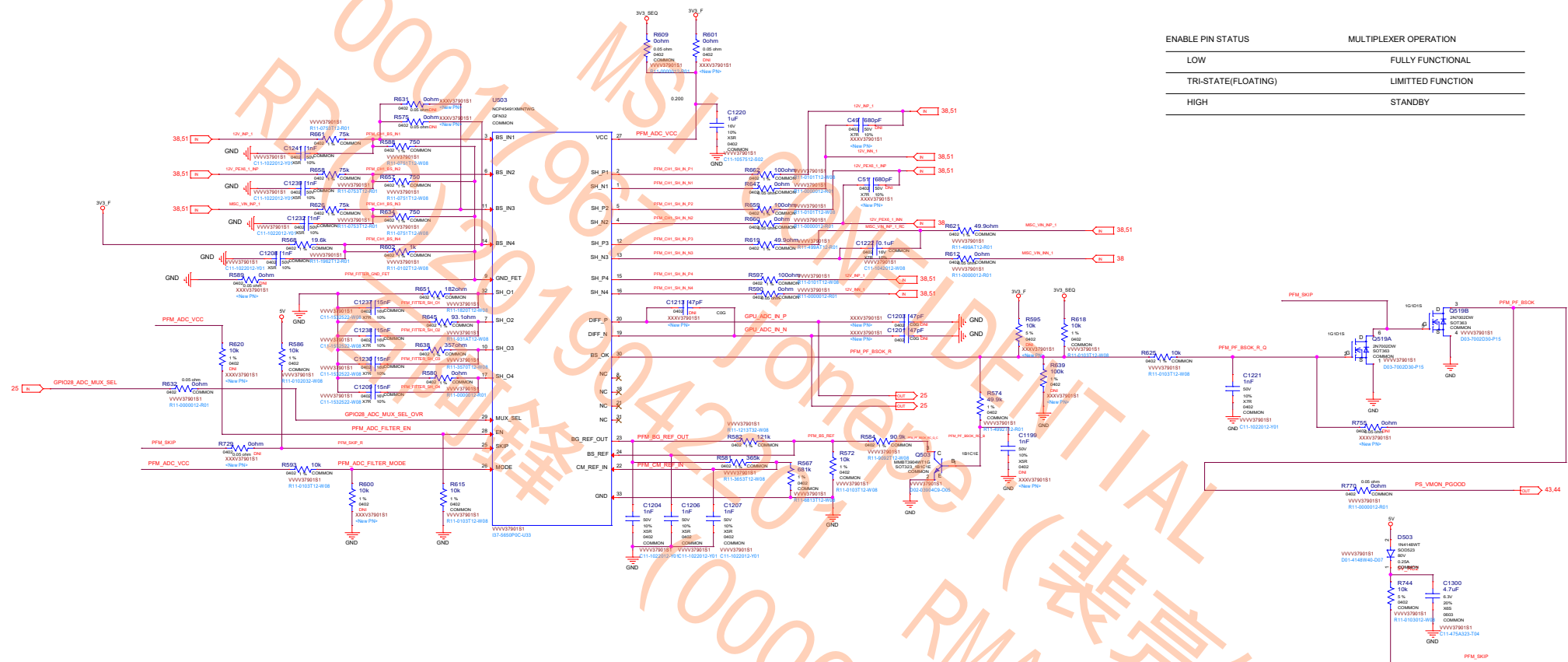
ADD LED



5V_LED

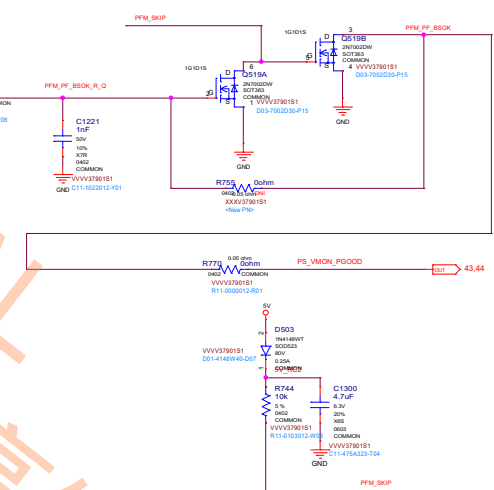






MODE PIN STATUS	MULTIPLEXER OPERATION
LOW	DEVICE A
TRI-STATE(FLOATING)	STAND-ALONE
HIGH	DEVICE B


ENABLE PIN STATUS	MULTIPLEXER OPERATION
LOW	FULLY FUNCTIONAL
TRI-STATE(FLOATING)	LIMITED FUNCTION
HIGH	STANDBY



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